



Ser. No. 09/417,527
Att'y Docket No. 2207/6926
Assignee: Intel Corporation

#17
RWB
5-7-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

PUTZOLU, David M.

Serial No. 09/417,527

Filed: October 13, 1999

For: METHOD AND SYSTEM FOR
DYNAMIC APPLICATION LAYER
GATEWAYS

Examiner: BURGESS, Barbara N.

Art Unit: 2157

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Technology Center 2100

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action mailed February 3, 2004 in the above-identified application, the Applicant respectfully submits the following remarks.

REMARKS

Claims 1-9 and 11-22 are pending in the application and stand rejected. Favorable reconsideration is respectfully requested in light of the following remarks.

Claims 1, 4-5, 7, 9, 13, 15, 17, 21 and 22 continue to be rejected under 35 USC 103(a) as being unpatentable over Yamamoto et al. (US 6,282,563 B1; hereafter, "Yama") in view of Li et al. (US 6,119,165; hereafter, "Li").

Independent claim 1 requires:

- moving an agent from a first device to a target device;
- re-routing relevant traffic to the target device; and